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NEW SYSTEM VARIABLE (ROM 1.2)

5B70H
ANYIV

This address is jumped to when any interrupt occurs. Normally ANYIV contains the address of the ROM's interrupt handler (0049H). On entry to the address stored in ANYIV, the B register holds the LMPR state when the interrupt occurred, the C register holds the status port state 6-9 microseconds after the interrupt occurred. The AF, BC and HL register pairs have been stacked with the values they had when the interrupt occurred. The system variables (page 0) is paged in at 4000H. (This may have switched out the stack!) HL holds the contents of ANYIV. Your code will be entered 19-23 microseconds after the interrupt signal occurred.

For example:

```
LD    A,B
OUT   (LMPR),A
POP   HL
POP   BC
POP   AF
EI
RET
```

No keyscan or other interrupt events will happen. Alternatively, JP back to 0049H (the ROM's interrupt handler), with the B register unchanged.