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Project # P1394.1

Project Title Standard for High Performance Serial Bus Bridges

Period Covered 8/96 - 1/97

Name, address, email, and FAX of submitter

Richard Scheel Sony Research Labs 3300 Zanker Rd. M/S SJ3D3

San Jose, CA 95134

email: dicks@lsi.sel.sony.com

fax: 408-955-5180

I. Current Status

Writing draft standard, developing technical details of some parts.

II. Achievements since last report
There has been a period of low activity. Some individual development work has continued, and the working group is resuming activity with the upcoming 3/19/97 meeting.

III. Plans for next period Continue technical development. Write additional sections of draft standard.

- IV. Schedule through RevCom submission
  - Date PAR Approved: 6/20/96, revised 9/96
  - Date/Plan for Requesting MSC Balloting Approval: TBD
  - Date/Plan for Ballot Close(s): TBD
  - Date/Plan for RevCom Submission(s): 12/31/97
- V. Reasons for any change in schedule
- VI. Key open issues and proposed solutions
  - reset notification / transaction integrity
  - heartbeat
- VII. Officers of WG (include phones, FAXs, and emails)

chair:

Richard Scheel Sony Research Laboratories M/S SJ3D3 3300 Zanker Rd. San Jose, CA 95134

voice: 408-955-4295 fax: 408-955-5180

email: dicks@lsi.sel.sony.com

editor:

Peter Johansson Congruent Software 3998 Whittle Ave. Oakland, CA 94602 voice: 510-531-5472 fax: 510-531-2942

email: pjohansson@aol.com

VIII. WG meeting schedule and agenda of next meeting

March 19-20 at Sony in San Jose Agenda:

- 1. Administrative
  - a) Minutes of October 7 1996 meeting

- b) Committee report to MSC
- 2. Updated draft document
- 3. Technical topics
  - a) Report of simulation project at Sony
  - b) Reset notification
- c) Deadlock & starvation avoidance4. Assignment of action items
- 5. Next meeting date

\_\_\_\_\_\_ Tel: (408) 955-4295 Dick Scheel Fax: (408) 955-5180 Sony US Research Labs LSI Systems Laboratory email: dicks@lsi.sel.sony.com 3300 Zanker Rd. M/S SJ3D3 0-San Jose, CA 95134-1940

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