## Molex 1394-95 6 ckt. I/O Differential/Single Ended Electrical Performance



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Max Bassler - Molex representative- IEEE 1394a + b groups Dave Brunker - presenter

1

IEEE1394-a/b meetings





## **Connector Only; Introduction**

- Data presented is for the connector only; no 1394 cable is attached. This allows for a focused and detailed review of connector only performance.
- The differential electrical performance for impedance, crosstalk, propagation delay, and risetime degradation is included.

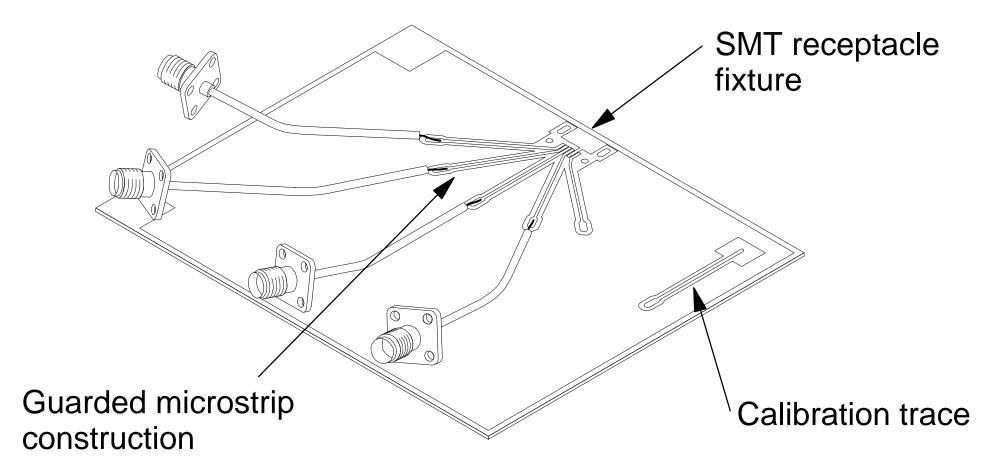


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- Time domain based empirical evaluation
- The test fixture is constructed as follows:
  - Semi-rigid coax feeds (100 ohms differential) to provide an interface to the PCB
  - Printed circuit board (110 ohms differential traces) to provide an interface to the receptacle connector including the effects of pad/via loading
  - Plug terminations
    - TDR time domain reflectometry measurement resistive terminations (110 ohms differential)
    - TDT time domain through measurement semi-rigid coax (100 ohms differential) interface

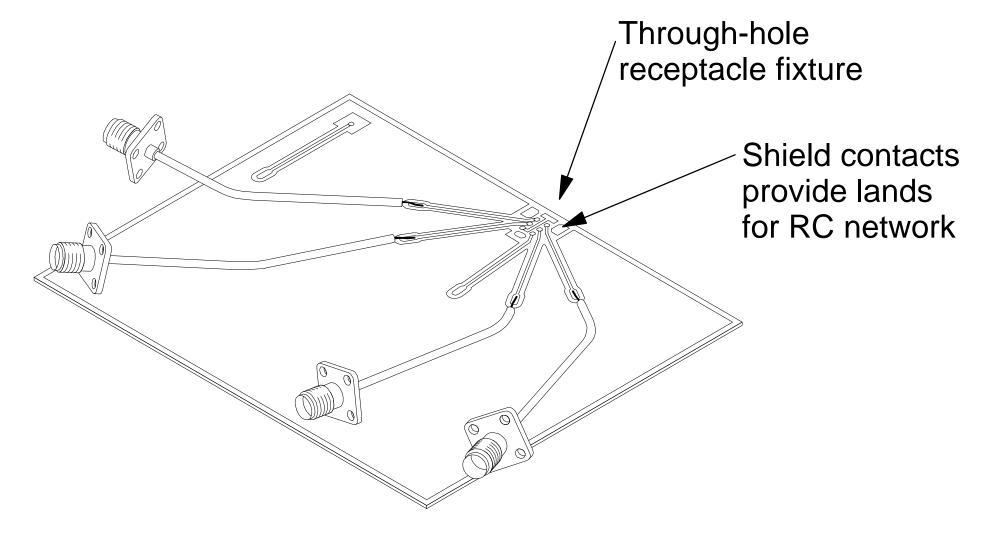








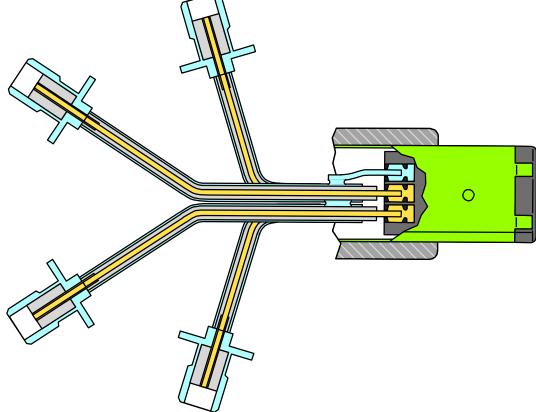




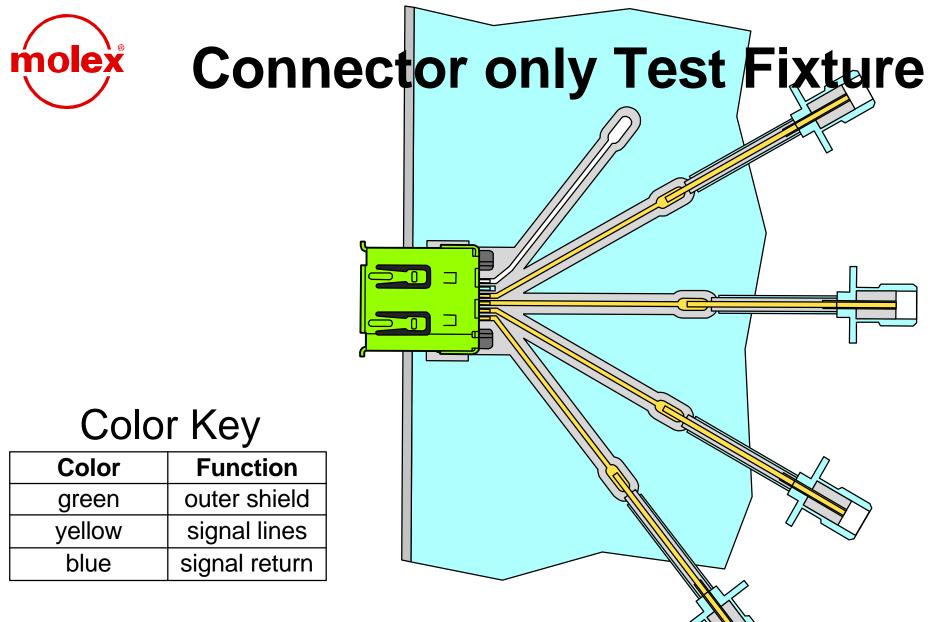




Colo	r Key
Color	Function
green	outer shield
yellow	signal lines
blue	signal return











### Test Conditions; Differential Impedance

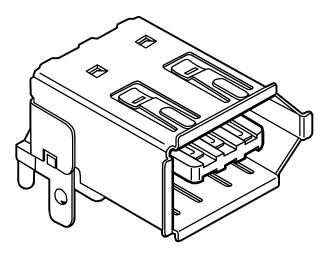
Load Impedance	110 ohms differential (resistive load)
Source impedance	110 ohms differential (2 x 55 ohm PCB)
Test Risetime (10% - 90%) (Equipment used was the Tektronix	26ps*, 150ps, 250ps, 500ps*, 1000ps
11801B Digital Sampling Oscilloscope with SD-24 TDR/DTDR heads) (The filtering function on the 11801B was used to generate the noted risetimes)	(*The maximum and minimum impedance values are listed for these risetimes in the results tables)
Vg and Vp configuration	Both receptacle and plug side are terminated with 30 ohms between Vg and Vp. Coax are shields tied to Vg.
External Shield (Receptacle to PCB ground plane network)	R = 1 Megohm shunt C = 2 x 0.05 microfarad parallel ceramic chips @ 50 DCWV shunt

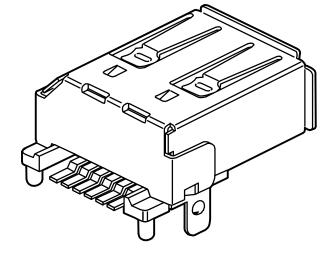




### SMT/Flat Differential Impedance Results

	Drive Pair A		<u>Drive Pair B</u>	
Risetime (ps)	Maximum Differential Z (ohms)	Minimum Differential Z (ohms)	Maximum Differential Z (ohms)	Minimum Differential Z (ohms)
tr = 26 ps	102.5 ohms	61.4 ohms	105.9 ohms	62.4 ohms
tr = 500 ps	110.2 ohms	88.8 ohms	100.9 ohms	88.8 ohms



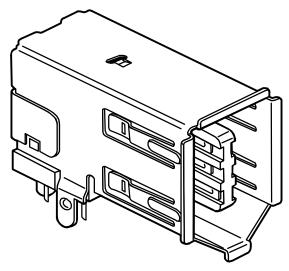


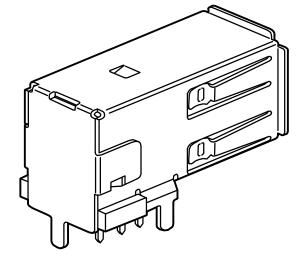




### Through Hole/Upright Differential Impedance Results

	Drive Pair A		<u>Drive Pair B</u>	
Risetime (ps)	Maximum Differential Z (ohms)	Minimum Differential Z (ohms)	Maximum Differential Z (ohms)	Minimum Differential Z (ohms)
tr = 26 ps	126.0 ohms	60.1 ohms	146.3 ohms	63.4 ohms
tr = 500 ps	102.1 ohms	93.5 ohms	101.6 ohms	94.6 ohms









### Test Conditions; Differential Crosstalk

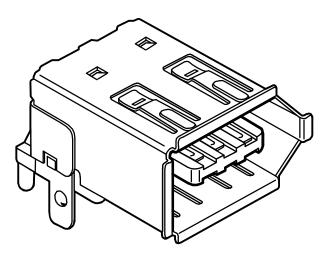
Load Impedance	100 ohms differential (2 x 50 ohm coax)
Source impedance	110 ohms differential (2 x 55 ohm PCB)
Test Risetime (10% - 90%) Vapplied = 500 mV p-p (Equipment used was the Tektronix 11801B Digital Sampling Oscilloscope with SD-24 TDR/DTDR and SD-22 sampling heads)	150ps 250ps 433ps
Vg and Vp configuration	Both receptacle and plug side are terminated with 30 ohms between Vg and Vp. Coax shields are tied to Vg.
External Shield (Receptacle to PCB ground plane network)	R = 1 Megohm shunt C = 2 x 0.05 microfarad parallel ceramic chips @ 50 DCWV shunt

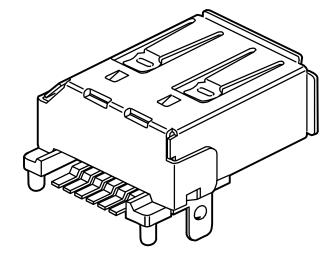




### SMT/Flat Differential Crosstalk Results

<u>Risetime</u>	<u>Near End</u> (NEXT)	<u>Far End</u> (FEXT)
150 ps	4.2%	2.0%
250 ps	3.6%	1.0%
433 ps	1.9%	0.5%



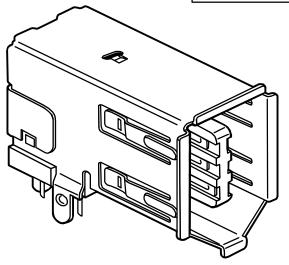


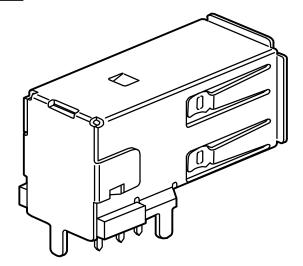




### Through Hole/Upright Differential Crosstalk Results

<u>Risetime</u>	<u>Near End</u> (NEXT)	<u>Far End</u> <u>(FEXT)</u>
150 ps	5.6%	2.0%
250 ps	5.2%	1.3%
433 ps	3.8%	0.9%









### Test Conditions; Differential Risetime Degradation and Propagation Delay

Load Impedance	100 ohms differential (2 x 50 ohm coax)
Source impedance	110 ohms differential (2 x 55 ohm PCB)
Test Risetime (10% - 90%) Vapplied = 500 mV (Equipment used was the Tektronix 11801B Digital Sampling Oscilloscope with SD-24 TDR/DTDR and SD-22 sampling heads)	26ps
Vg and Vp configuration	Both receptacle and plug side are terminated with 30 ohms between Vg and Vp. Coax shields tied to Vg.
External Shield (Receptacle to PCB ground plane network)	R = 1 Megohm shunt C = 2 x 0.05 microfarad parallel ceramic chips @ 50 DCWV shunt





### Test Conditions; Differential Risetime Degradation and Propagation Delay

The <u>risetime degradation</u> data was calculated using the 10% to 90% levels of the input and output pulse to the connector. Where:

tr,connector = [(tr,DUT + test system)e2 - (tr,test system)e2]e1/2

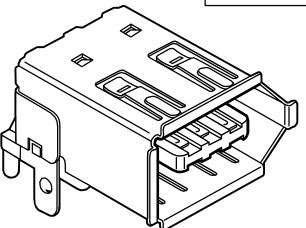
- From the generated connector risetime degradation value, a first order filter approximation was used (BW = 0.35/tr) to convert this risetime value to a bandwidth (-3dB cutoff).
- The propagation delay measurements were taken at both the 10% and 50% levels with 10% levels minimizing filter effects.

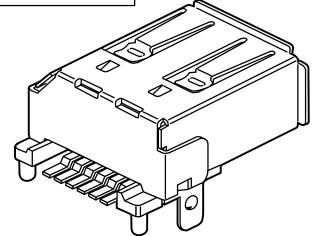




### SMT/Flat Differential Risetime Degradation Results

	Connector Risetime Degradation; 10%-90% (psec)	Connector Bandwidth (GHz)
Pair A	40.3 ps	8.7 GHz
Pair B	47.9 ps	7.3 GHz



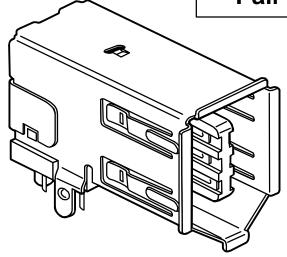


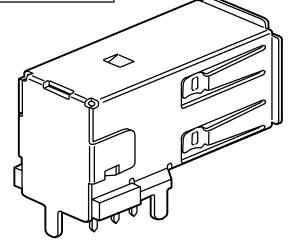




### Through Hole/Upright Differential Risetime Degradation Results

	Connector Risetime Degradation; 10%-90% (psec)	Connector Bandwidth (GHz)
Pair A	113.8 ps	3.1 GHz
Pair B	114.1 ps	3.1 GHz



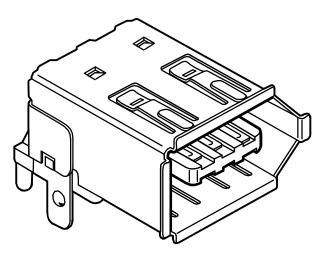


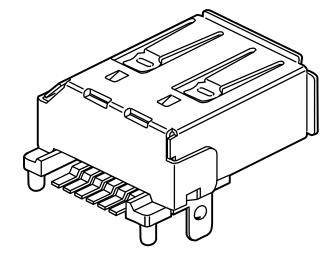




### **SMT/Flat Differential Propagation Delay Results**

	Connector Differential Propagation Delay 10%	Connector Differential Propagation Delay 50%
Pair A	178.7 ps	183.2 ps
Pair B	176.1 ps	183.7 ps



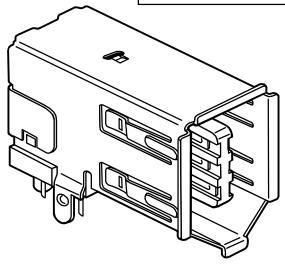


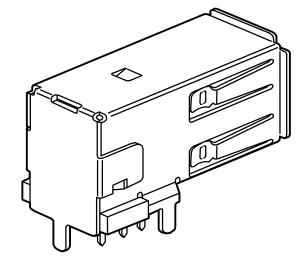




### Through Hole/Upright Differential Propagation Delay Results

	Connector Differential Propagation Delay 10%	Connector Differential Propagation Delay 50%
Pair A	169.4 ps	180.4 ps
Pair B	181.5 ps	199.6 ps







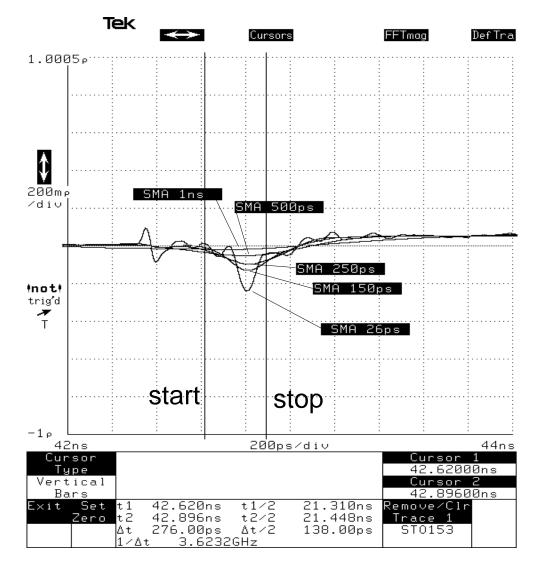
# Recommendations/Conclusions

- <u>Differential specification</u>
  Limit 1394.a differential system risetimes to no faster than 0.5 nanoseconds
- <u>Single-ended specification</u> Given that only 10-20 ns risetimes are necessary to service the "Speed Signaling" function, limit 1394.a single-ended (Speed Signaling) system risetimes to no faster than 3 nanoseconds





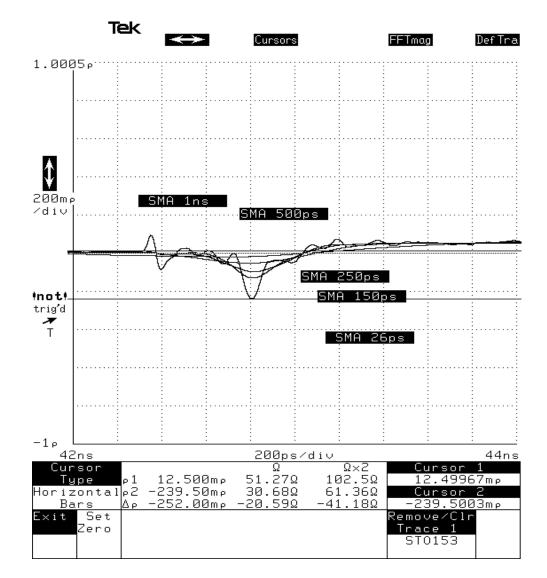
### SMT Receptacle mated with plug (TPA-TPA\*) Differential Impedance







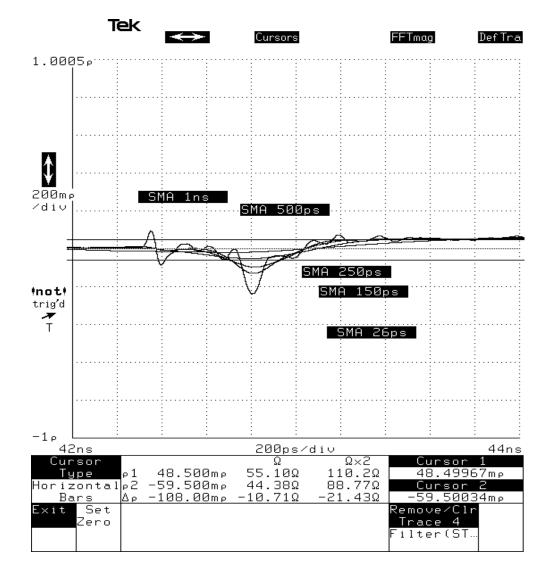
### SMT Receptacle mated with plug (TPA-TPA\*) Differential Impedance @ tr = 26 ps







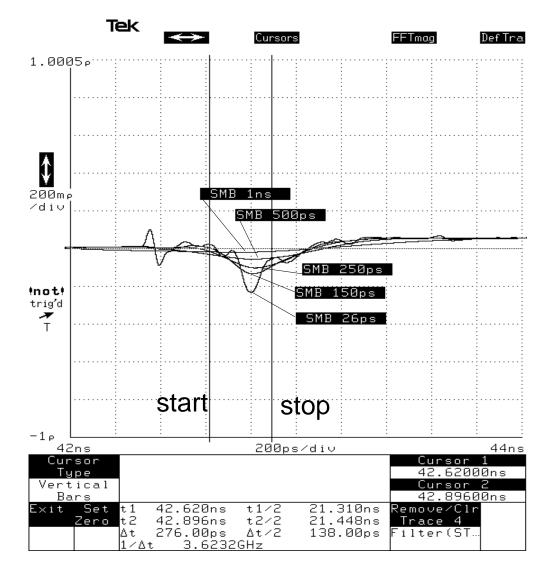
### SMT Receptacle mated with plug (TPA-TPA\*) Differential Impedance @ tr = 500 ps







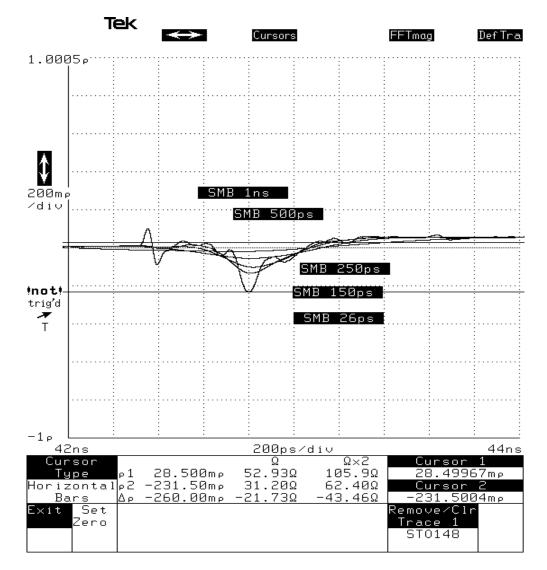
#### SMT Receptacle mated with plug (TPB-TPB\*) Differential Impedance







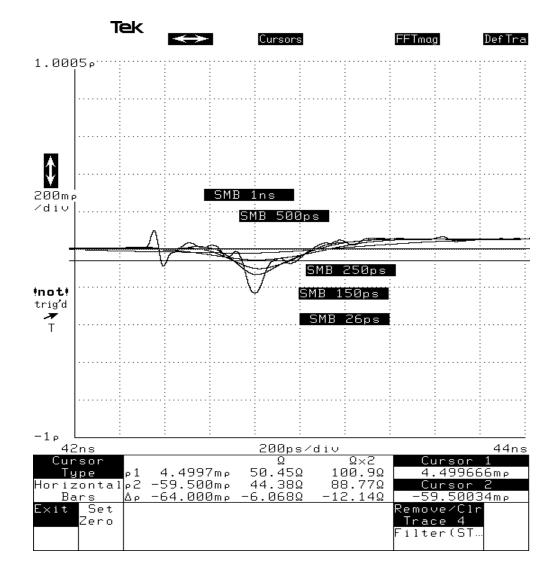
#### SMT Receptacle mated with plug (TPB-TPB\*) Differential Impedance @ tr = 26 ps







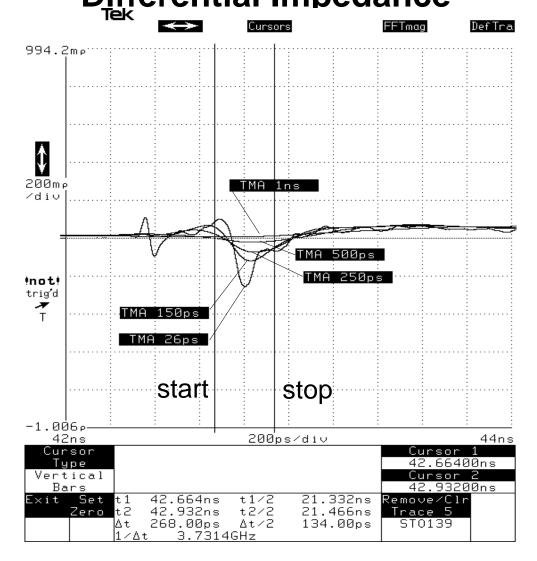
### SMT Receptacle mated with plug (TPB-TPB\*) Differential Impedance @ tr = 500 ps





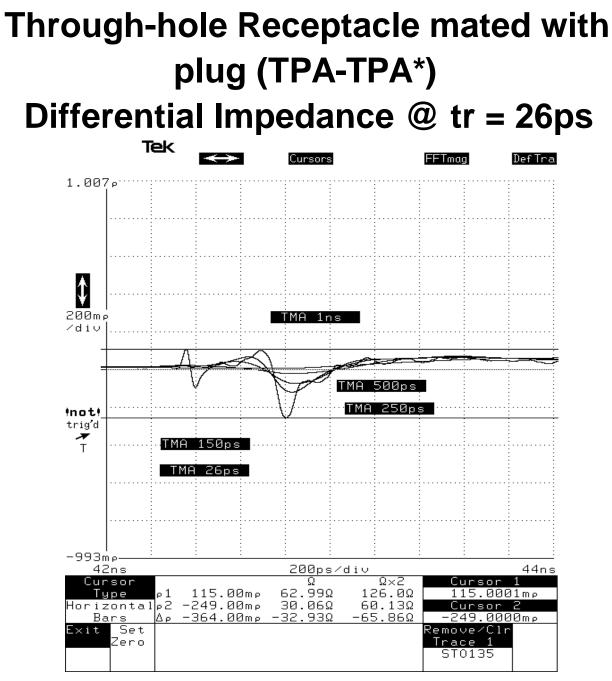


### Through-hole Receptacle mated with plug (TPA-TPA\*) Differential Impedance



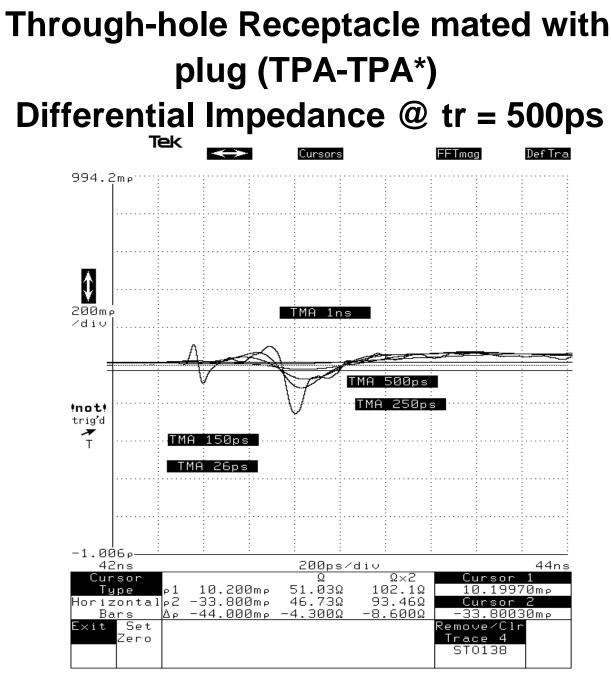








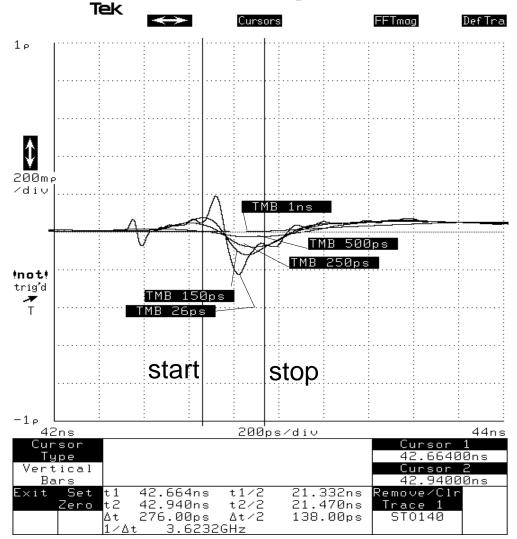






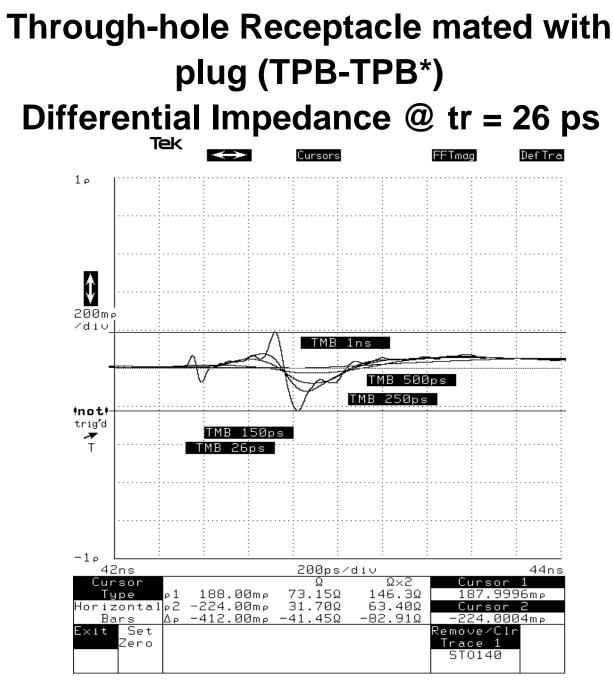


### Through-hole Receptacle mated with plug (TPB-TPB\*) Differential Impedance



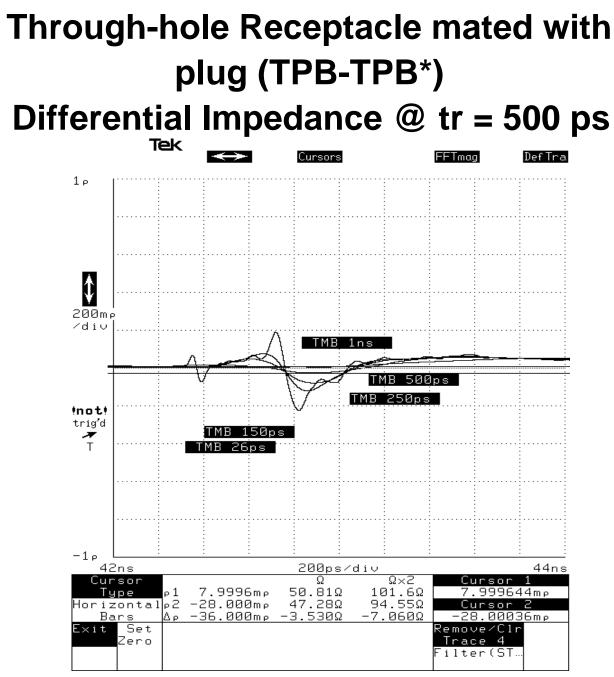




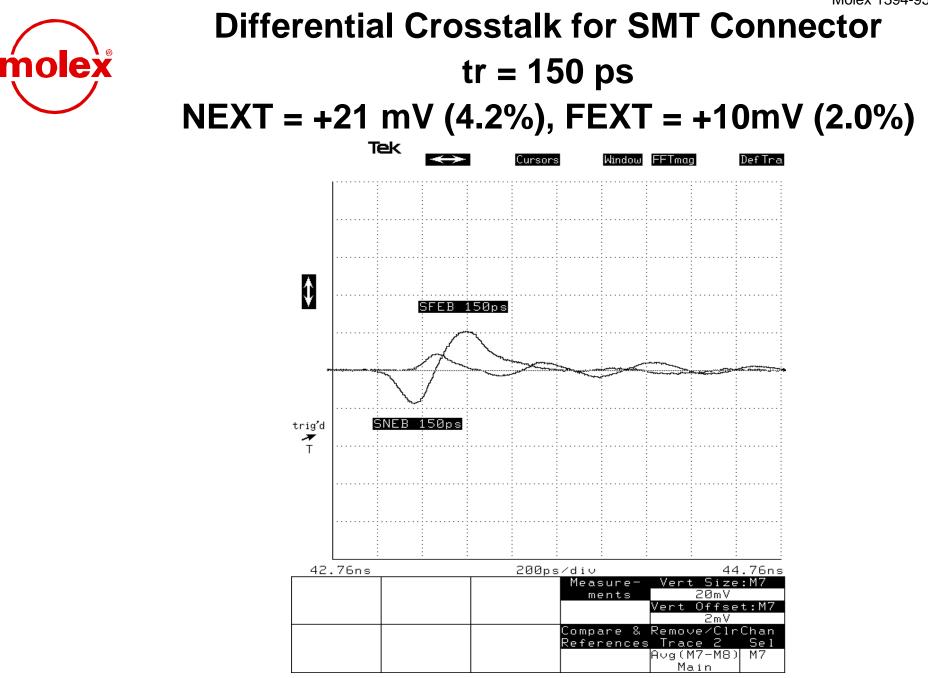












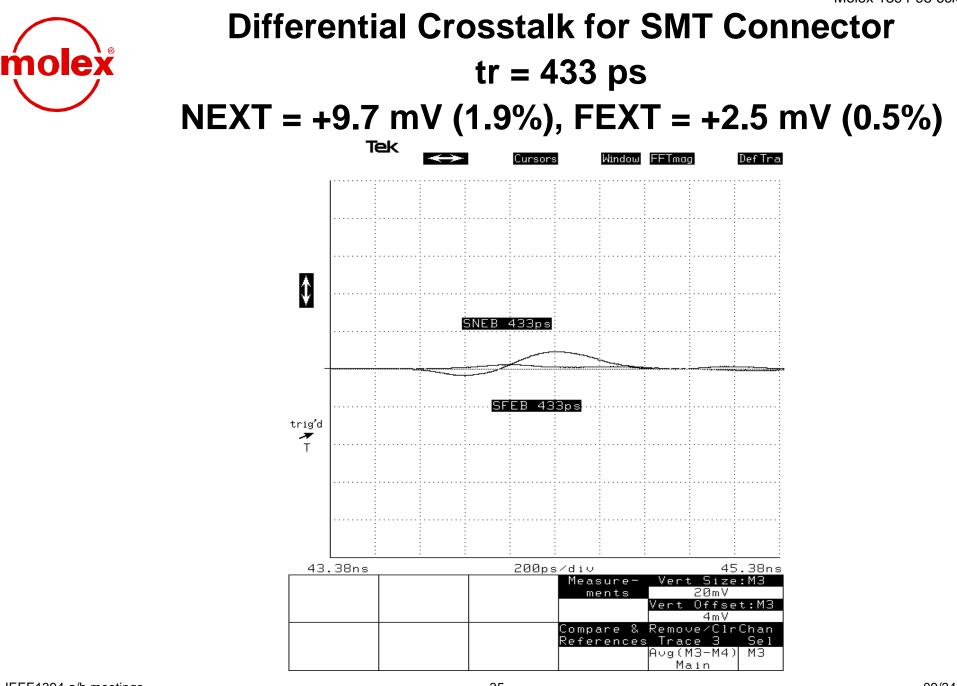


#### **Differential Crosstalk for SMT Connector** tr = 250 ps NEXT = +18 mV (3.6%), FEXT = +4.9 mV (1.0%) Tek $\leftrightarrow$ L FFTmag Cursors DefTra 5U \$ 1U SNEB 250ps ∕div ኡ SFEB 250ps inot! trig'd T -50 42.88ns 200ps/div 44.88ns Horz Mag Measure ments $1 \times$ Horz Pos Øpts Compare & Remove∕ClrPan References Trace Zoom ST0165 0n 34

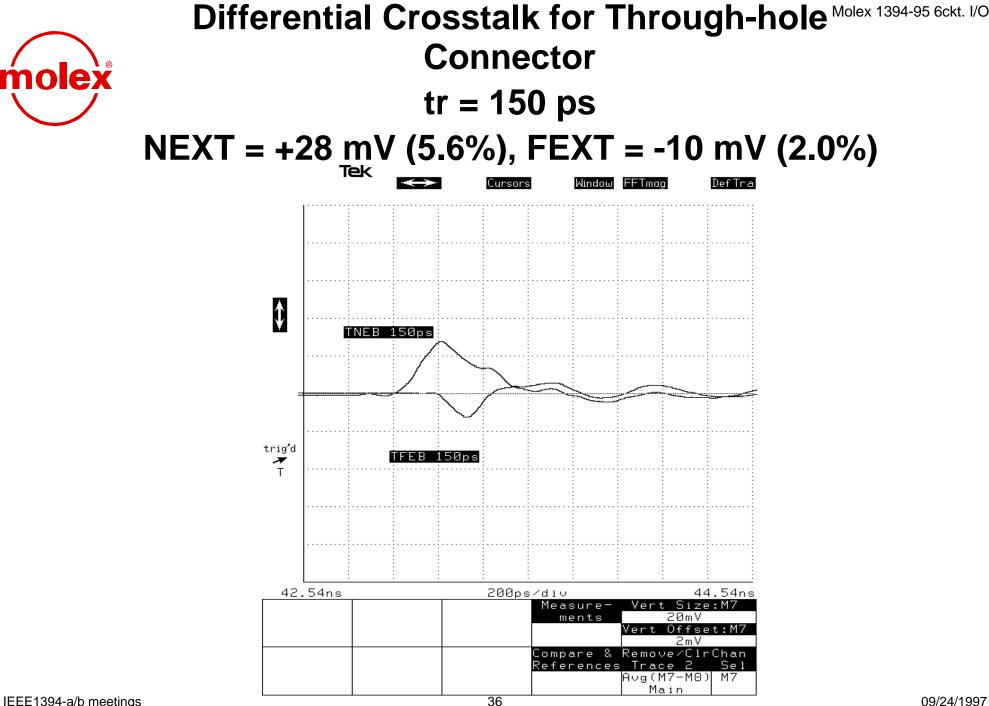


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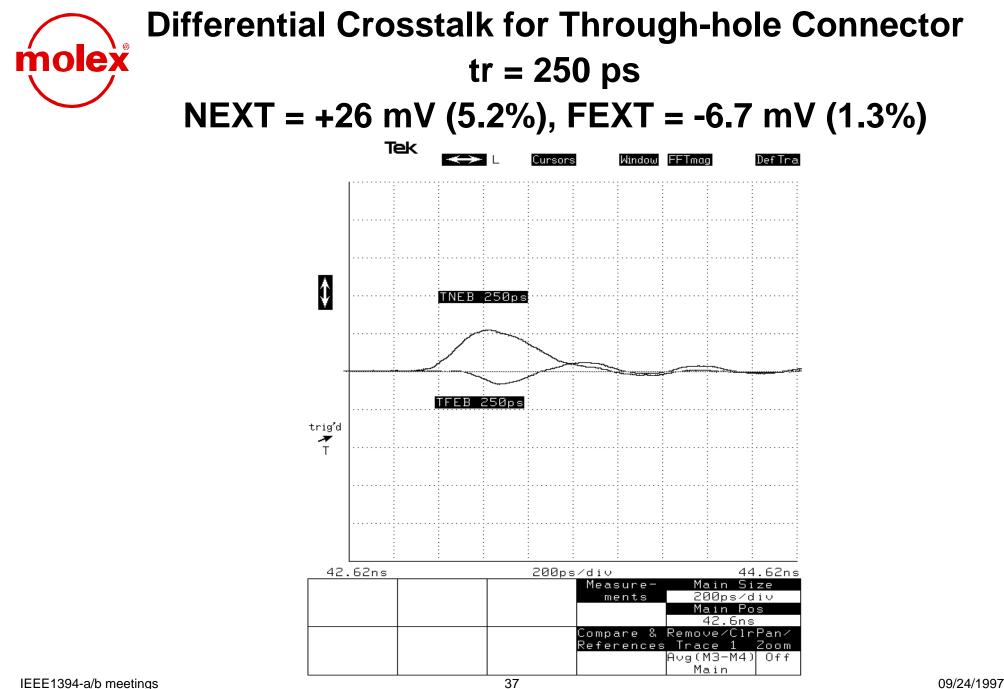




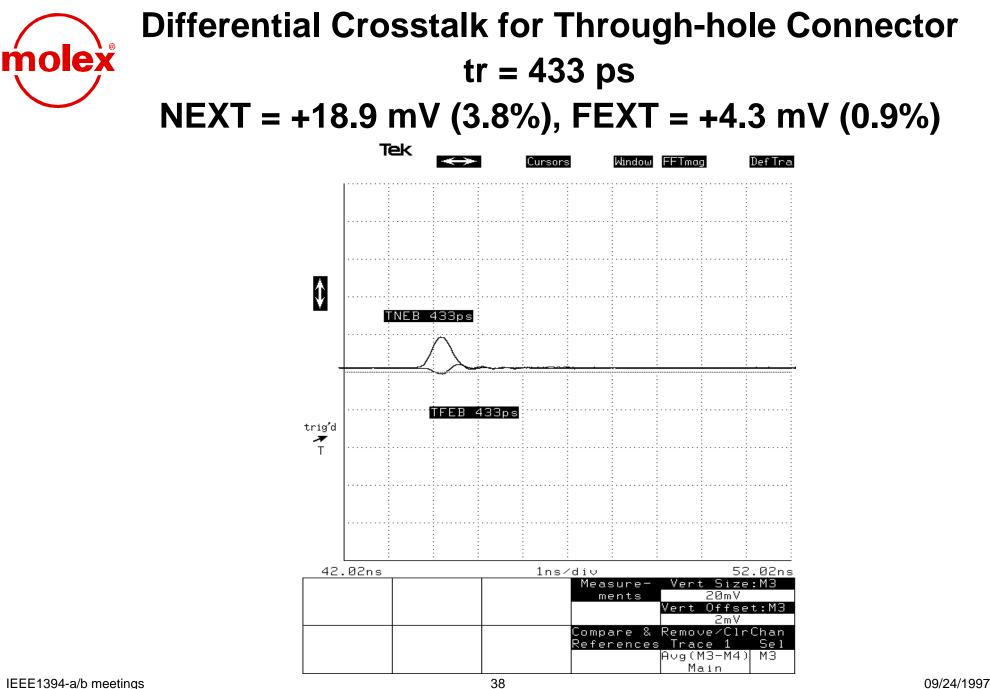








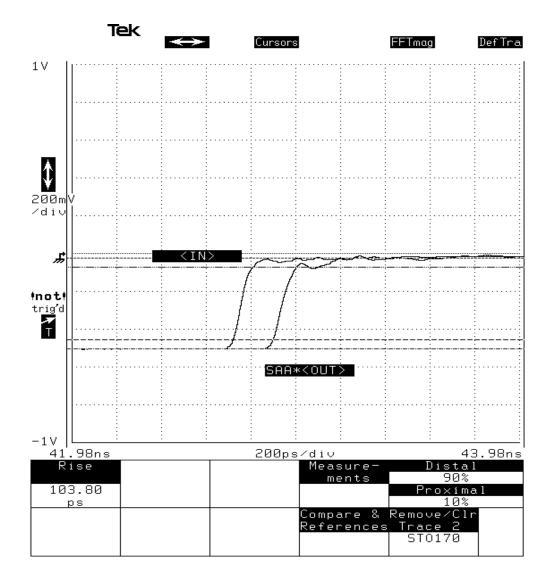








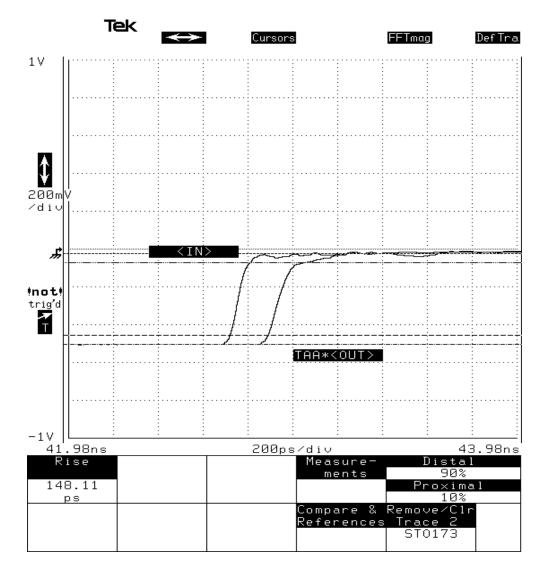
### SMT Connector Typical Transmission Through







### Through-hole Connector Typical Transmission Through







## Test Conditions; Single Ended Crosstalk

Load Impedance	A, A*, B, B*; 50 ohms
Source impedance	A, A*, B, B*; 50 ohms
Test Risetime (10% - 90%)	tr = 3 ns
Vg and Vp configuration	Near End Crosstalk: Vg - 0 ohms to signal ground Vp - open Far End Crosstalk: Vg - 0 ohms to signal ground Vp - 50 ohms to signal ground
External Shield (Receptacle to PCB ground plane network)	R = 1 Megohm shunt C = 2 x 0.05 microfarad parallel ceramic chips @ 50 DCWV shunt
Crosstalk Ratio	Ratio of the maximum voltage peak to the input voltage. Crosstalk % = Vpeak / Vinput



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### Assembly Performance; Single Ended Crosstalk

#### Far End Crosstalk

Cable Type	Cable Assembly Length	XAB	XAB*	XA*B	XA*B*
D8D	0.07 m	2.7%	2.5%	2.7%	2.5%
D8D	0.7 m	2.9%	2.7%	2.7%	2.7%
D8D	2.0 m	2.7%	3.1%	2.9%	2.7%
D8D	4.5 m	2.7%	3.1%	2.7%	2.5%

#### **Near End Crosstalk**

Cable Type	Cable Assembly Length	XAB	XAB*	XA*B	XA*B*
D8D	0.07 m	3.5%	3.3%	2.7%	3.1%
D8D	0.7 m	4.7%	4.3%	4.7%	4.7%
D8D	2.0 m	3.5%	3.3%	1.9%	3.7%
D8D	4.5 m	2.9%	4.1%	2.7%	4.3%

Molex D8D cable construction employs internal pair shelds which are isolated from the external shield and internal pair shields are commoned to each other.

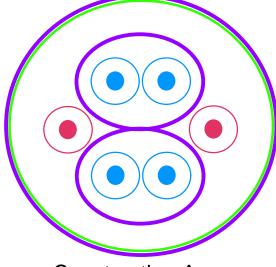




### Cable Construction A vs. Cable Construction X; Crosstalk Comparison

Construction A	Near End Single Ended Crosstalk
100 MHz	-26.9 dB (4.5 %)
200 MHz	-25.3 dB (5.4 %)
400 MHz	-15.8 dB (16.2 %)

Construction	Near End Single Ended Crosstalk
100 MHz	-53.7 dB (0.2 %)
200 MHz	-48.4 dB (0.4 %)
400 MHz	-24.4 dB (6.0 %)



Construction A per 1394-95 specification