Subj: MAX_ROM Proposal
Date: 99-04-28 04:26:57 EDT
From: brianb@vcd.hp.com (Brian Batchelder)
To: PJohansson@aol.com (Peter Johansson)

Peter,

I am forwarding this proposal to the 1394a committee through you as 1394a chairperson. It is the 1212r working groups proposal for the new "max_ROM" field. I have clipped the proposal directly from our most recent draft revision of the 1212r spec.

Although parts of the bus information block other than the bus_name and eui_64 fields shall be specified by the relevant bus standard, the inclusion of a 4-bit max_ROM field is strongly recommended. If the bus standard defines max_ROM, the field shall specify the size and alignment of read requests supported by configuration ROM within the first kilobyte. Suggested encoding for the max_ROM field is given by the table below.

max_ROM	Description
0	Quadlet read requests are supported. This encoding is suggested for legacy devices and should not be reported by devices compliant with this standard.
1	Quadlet read requests and block read requests aligned on 64-byte addresses with a data length of 64 bytes are supported.
2	Quadlet read requests and block read requests aligned on quadlet addresses with a data length less than or equal to 1024 bytes are supported.

NOTE – Devices that report a max_ROM value of zero should support block read requests of the bus information block (if possible) even if block read requests are not supported for any other portion of configuration ROM.

Thank you for your consideration,

Brian

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